

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY**

Examination Centre: Main Building UCE (A), O.U.
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M. E. (ECE & Mech.) II - Semester (Main) Examination August 2011

VASAVI COLLEGE OF ENGG.

EXAMINATION TIME-TABLE

Time : 2.00 PM to 5.00 PM

“Centre : University College of Engineering, Osmania University, Hyderabad – 7”

Date and Day	E.C.E. (Embedded Systems & VLSI Design)	MECHANICAL (Advanced Design and Manufacturing)
	Regular II – Semester	Regular II – Semester
16-08-2011 TUESDAY	VLSI Physical Design	Product Design & Process Planning
18-08-2011 THURSDAY	Principles of VLSI System Design	Computer Aided Mechanical Design and Analysis
20-08-2011 SATURDAY	Low Power VLSI Design	Optimization Techniques
22-08-2011 MONDAY	CPLD & FPGA Architectures and Applications	Advanced Non-Destructive Evaluation Techniques
24-08-2011 WEDNESDAY	Real Time Operating Systems	Robotic Engineering
26-08-2011 FRIDAY	Design for Testability	Mechanics of Composite Materials
29-08-2011 MONDAY	= = = = =	Metal Processing Science

Note: This Time-Table is also applicable for Pre-Ph.D. students registered for August 2011 Examination.

DIRECTOR OF EVALUATION
EXAMINATION CELL